

**SAMPLE — NOT LEGAL ADVICE.** This response letter was generated automatically from publicly available analysis. It has NOT been reviewed by a licensed attorney and SHOULD NOT BE SENT to any party without substantial review and customization by qualified patent counsel. Use as a starting point only.

[Your Name]

[Your Title]

[Your Company]

[Street Address, City, State ZIP]

May 14, 2026

[Opposing Counsel Name]

[Firm Name]

[Address]

**Re: U.S. Patent No. 8148962 — Response to Assertion of Infringement**

Dear Counsel,

We acknowledge receipt of your correspondence asserting infringement of U.S. Patent No. 8148962 (the "8148962 Patent"). After preliminary review, we have substantial concerns about the validity, enforceability, and scope of the asserted claims, summarized below. We reserve all rights and defenses.

## **1. Subject Patent — Summary**

Analysis of U.S. Patent 8,148,962

Washington, D.C. - A detailed analysis of United States Patent 8,148,962, titled "Transient load voltage regulator," reveals a technology aimed at stabilizing power supply voltage in integrated circuits facing varying electrical loads. This patent, issued on April 3, 2012, has seen a transfer of ownership and is currently assigned to Palisade Technologies LLP.

The patent was originally filed on May 12, 2009, with inventor Tomer Shaul Elran. At the time of issuance, the assignee was SanDisk IL Ltd. However, subsequent assignment records from the U.S. Patent and Trademark Office (USPTO) indicate the transfer to Palisade Technologies LLP. The core of the...

## **2. Validity Concerns under 35 U.S.C. § 102 — Prior Art**

We have identified prior-art references that, in our preliminary view, anticipate one or more asserted claims of the 8148962 Patent:

As a senior US patent analyst, I will now provide a detailed analysis of the most relevant prior art cited in US patent 8,148,962, "Transient load voltage regulator." The following analysis examines each cited reference for its potential to anticipate the claims of the '962 patent under 35 U.S.C. § 102.

An invention is anticipated under 35 U.S.C. § 102 if a single prior art reference discloses each and every element of a claimed invention, arranged as in the claim.

Analysis of Prior Art for US Patent 8,148,962

Here is an analysis of the prior art references cited in the '962 patent:

1. US Patent 5,867,015 (Corsi et al.)

- Full Citation: US Patent 5,867,015, "Low drop-out voltage regulator with PMOS pass element," filed December 19, 1996, and issued February 2, 1999.

- Brief Description: This patent describes a low drop-out (LDO) voltage regulator utilizing a PMOS pass transistor. It features a feedback control loop to maintain a stable output voltage. The primary focus is on achieving a low voltage difference between the input and output.

- Potential Anticipation of Claims: The...

### **3. Obviousness under 35 U.S.C. § 103**

Independent of § 102, we believe the asserted claims are obvious in view of combinations of prior art that a person having ordinary skill in the art would have been motivated to combine:

Analysis of Obviousness under 35 U.S.C. § 103 for U.S. Patent 8,148,962

Date of Analysis: May 13, 2026

Patent under Review: U.S. Patent 8,148,962 ("the '8962 patent")

Priority Date: May 12, 2009

This analysis considers whether the invention claimed in the '8962 patent would have been obvious to a Person Having Ordinary Skill in the Art (POSA) at the time the invention was made. A POSA in this field would typically possess a degree in electrical engineering and have several years of experience in designing analog integrated circuits, particularly power management circuits like low-dropout (LDO) regulators.

The central inventive concept of the '8962 patent, particularly as articulated in independent claim 1, is a voltage regulator architecture that controls a pass device using a signal derived from two currents supplied to a single current path. The first current is substantially constant, while the second current is variable. The magnitude of this second current is determined by a direct comparison between a stable reference voltage (the gate of a feedback transistor) and the...

### **4. Litigation History of the Patent**

Public records reflect that the 8148962 Patent has been the subject of the following litigation, which informs our view of the asserted claims and your client's enforcement posture:

- Palisade Technologies, LLP v. Micron Technology, Inc. et al. — 7:24-cv-00262 · U.S. District Court for the Western District of Texas · filed 2024-10-16 · Dismissed

### **5. Request**

In light of the foregoing, we request that your client (i) provide a detailed claim chart identifying each accused product or service and mapping every limitation of each asserted claim, (ii) identify any prior art known to your client, including any references cited during prosecution or reexamination, and (iii) substantiate the basis for any damages or licensing demand. We are

prepared to discuss the matter further once we have received and reviewed the foregoing.

Sincerely,

**[Your Name]**

**DISCLAIMER. This document is a machine-generated sample. The factual assertions, prior-art citations, and legal arguments above are AI-produced and may contain errors, omissions, or outdated information. Do not transmit this letter, in whole or in part, to any party. This is not legal advice; no attorney-client relationship is created by its existence. Consult a licensed patent attorney before responding to any patent-infringement assertion.**

*Generated May 14, 2026 by [ihatepatentrolls.com](http://ihatepatentrolls.com) — sample only.*